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	Filing Date		2004-01-26	
	First Named Inventor	Yolanta Beresnevichiene		
	Art Unit	2196		
	Examiner Name	CAO, DIEM K		
Attorney Docket Number		200207542-2		

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	4	Kumar, N. and Childers, B., Flexible Instrumentation for Software Dynamic Translation, Workshop on Exploring the Trace Space, during the International Conference of Supercomputing [online], 2003 [retrieved 2009-11-02], Retrieved from Internet: <URL http://www.naveenkumar.org/papers/traces_workshop-03.pdf > pp. 1-10.	<input type="checkbox"/>
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	6	Reddi et al., "Pin: A Binary Instrumentation Tool for Computer Architecture Research and Education," Workshop on Computer Architecture Education 2004 (WCAE-04), Munich, Germany, June 19, 2004, 8 pages.	<input type="checkbox"/>
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